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October 15, 2007

MAIL STOP ISSUE FEE
Commissioner For Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Re: Applicant(s): Debanjan Mukherjee; Jishnu Bhattacharjee; Qian Yu;
& Abhijit Phanse
Title: Analog Delay Elements
Serial No.: 10/724,443 Filed: 11/26/2003
Examiner: Ngo, Chuong D. Group Art Unit: 2193
Docket No.: M-15287 US Confirmation No. 1193

Dear Sir:

Transmitted herewith are the following documents in the above-identified application:

- (1) This Transmittal Letter (1 page);
- (2) Submission of Formal Drawings Transmission (1 page) and Four (4) pages of Replacement Drawings; and
- (3) Part B – Fee Transmittal.

Certificate of Transmission

I hereby certify that this correspondence is being sent via EFS Web to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on the date stated below.

Electronically filed

Tina Kavanaugh

October 15, 2007

Respectfully submitted,



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